

Education

1. **PhD in Computer Science** , October 2014 , Norwegian University of Science and Tech. (NTNU), Norway
2. **Master of Computer Science** , May 2003 , University of Arkansas, USA
3. **Bachelor of Electrical Engineering**, January 1999, Birzeit University, Palestine

Professional experience/Employment history

1. **Arab American University - Palestine**
 - Head of the Computer Systems Engineering Department, 2012 – 2016, 2017 - now
 - Assistant of Vice President for Academic Affairs, 2016 - 2017
 - Assistant Professor, Department of Computer Systems Engineering, 2014 – now
 - Lecturer, Department of Computer Systems Engineering, 2012 – 2014
 - Instructor, Faculty of Engineering and IT, 2003 – 2008
2. **Miriam AS - Norway**
 - PhD Scholar, 2008 – 2011
3. **Birzeit University - Palestine**
 - Computer Labs' Administrator, 2000 – 2001
 - Teaching Assistant, 1999 – 2000

Taught Courses

1. Advanced Object Oriented Programming
2. Advanced Computer Architecture
3. Network Programming
4. Web Programming
5. Embedded Systems

Publications

1. Hamarsheh A. and Eleyat, M., *Performance Analysis of AIN-PT, AIN-SLT and SIIT Network-Based Translators*, In: Xhafa F., Caballé S., Barolli L. (eds) *Advances on P2P, Parallel, Grid, Cloud and Internet Computing*. 3PGCIC 2017. Lecture Notes on Data Engineering and Communications Technologies, vol 13. Springer, Cham.
2. Eleyat, M., et al., *Parallel algorithms for the maximum flow problem with minimum lot sizes*, in *Operations Research Proceedings 2011*, D. Klatte, H.-J. Lüthi, and K. Schmedders, Editors. 2012, Springer Berlin Heidelberg. p. 83-88.
3. Eleyat, M. and L. Natvig, *IPM based sparse LP solver on a heterogeneous processor*. *Computational Management Science*, 2012. **9**(2): p. 287-299.
4. Eleyat, M., L. Natvig, and J. Amundsen. *Cache-aware matrix multiplication on multicore systems for IPM-based LP solvers*. in *Computer Science and Information Systems (FedCSIS), 2011 Federated Conference on*. 2011.
5. Haugland, D., M. Eleyat, and M. Hetland, *The Maximum Flow Problem with Minimum Lot Sizes*, in *Computational Logistics*, J. Böse, et al., Editors. 2011, Springer Berlin Heidelberg. p. 170-182.
6. Eleyat, M. and L. Natvig, *Implementation of a linear programming solver on the Cell BE processor*. *Procedia Computer Science*, 2010. **1**(1): p. 1055-1064.
7. Eleyat, M. and L. Natvig. *Mixed-Precision Parallel Linear Programming Solver*. in *Computer Architecture and High Performance Computing (SBAC-PAD), 2010 22nd International Symposium on*. 2010.
8. Ibrahim, Mujahed and Apon, Amy W. *Sorting Using MPI and PVFS on Workstation Clusters*. in *Proceedings of the International Conference on Parallel and Distributed Processing Techniques and Applications (PDPTA), 2003*. P. 151-156.